

## VN16BSP

## ISO HIGH SIDE SMART POWER SOLID STATE RELAY

TYPE	VDSS	R <sub>DS(on</sub> )	Ιουτ	Vcc
VN16BSP	40 V	0.06 Ω	5.6 A	26 V

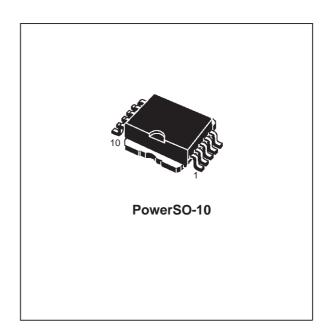
- MAXIMUM CONTINUOUS OUTPUT CURRENT :20A @ T<sub>c</sub> = 85°C
- 5V LOGIC LEVEL COMPATIBLE INPUT
- THERMAL SHUT-DOWN
- UNDER VOLTAGE PROTECTION
- OPEN DRAIN DIAGNOSTIC OUTPUT
- INDUCTIVE LOAD FAST DEMAGNETIZATION
- VERY LOW STAND-BY POWER DISSIPATION

#### DESCRIPTION

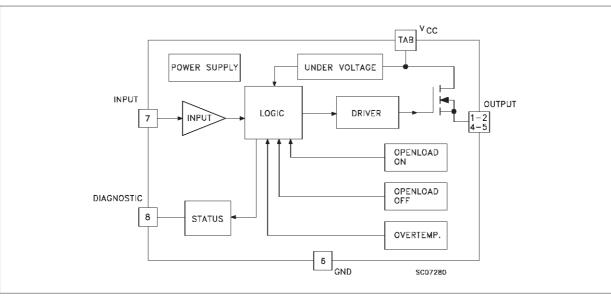
The VN16BSP is a monolithic device made using SGS-THOMSON Vertical Intelligent Power Technology, intended for driving resistive or inductive loads with one side grounded.

Built-in thermal shut-down protects the chip from over temperature and short circuit.

The open drain diagnostic output indicates: open load in off state and in on state, output shorted to  $V_{CC}$  and overtemperature.



Fast demagnetization of inductive loads is archivied by negative (-18V) load voltage at turn-off

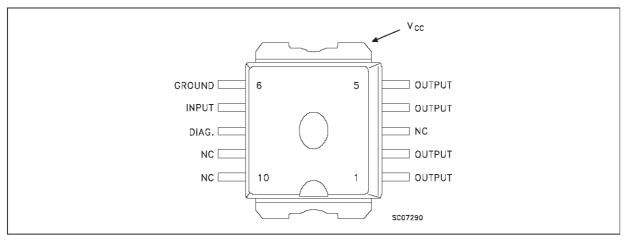


## **BLOCK DIAGRAM**

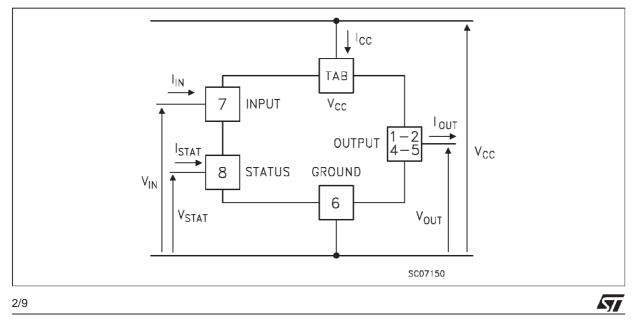
#### **ABSOLUTE MAXIMUM RATING**

Symbol	Parameter	Value	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	40	V
I <sub>OUT</sub>	Output Current (cont.) at T <sub>c</sub> = 85 °C	20	A
I <sub>OUT</sub> (RMS)	RMS Output Current at T <sub>c</sub> = 85 °C	20	A
I <sub>R</sub>	Reverse Output Current at $T_c = 85 \ ^{\circ}C$ (f > 1Hz)	-20	Α
lin	Input Current	±10	mA
-V <sub>CC</sub>	Reverse Supply Voltage	-4	V
I <sub>STAT</sub>	Status Current	±10	mA
Vesd	Electrostatic Discharge (1.5 kΩ, 100 pF)	2000	V
Ptot	Power Dissipation at $T_c = 25$ °C	82	W
Tj	Junction Operating Temperature	-40 to 150	°C
T <sub>stg</sub>	Storage Temperature	-55 to 150	°C

#### **CONNECTION DIAGRAMS**



#### CURRENT AND VOLTAGE CONVENTIONS



### THERMAL DATA

	Thermal Resistan	ce Junction-case	Max	1.5	°C/W
	Thermal Resistan	ce Junction-ambient (\$)	Max	50	°C/W
(\$) When mour	nted using minimum reco	nmended pad size on FR-4 board			

**ELECTRICAL CHARACTERISTICS** (8 < V<sub>CC</sub> < 16 V; -40  $\leq$  T<sub>j</sub>  $\leq$  125  $^oC$  unless otherwise specified) POWER

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vcc	Supply Voltage		6	13	26	V
ln(*)	Nominal Current	$T_{c} = 85 \ ^{o}C \ V_{DS(on)} \leq 0.5 \ V_{CC} = 13 \ V$	5.6		8.8	А
Ron	On State Resistance	$I_{OUT} = In  V_{CC} = 13 \ V  T_j = 25 \ ^{o}C$	0.038		0.06	Ω
ls	Supply Current	$Off \ State  V_{CC} = 13 \ V  T_j \geq 25 \ ^oC$		25	50	μA
VDS(MAX)	Maximum Voltage Drop	$I_{OUT} = 20 \text{ A}$ $V_{CC} = 13 \text{ V}$ $T_c = 85 ^{\circ}\text{C}$	1		1.8	V
Ri	Output to GND Internal Impedance	$T_j = 25 \ ^{\circ}C$	5	10	20	KΩ

#### SWITCHING

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
td(on) <b>(^)</b>	Turn-on Delay Time Of Output Current	$R_{load} = 1.6 \Omega$	5	50	500	μs
t <sub>r</sub> (^)	Rise Time Of Output Current	$R_{load} = 1.6 \Omega$	40	100	680	μs
t <sub>d(off)</sub> (^)	Turn-off Delay Time Of Output Current	$R_{load} = 1.6 \Omega$	10	100	500	μs
t <sub>f</sub> (^)	Fall Time Of Output Current	$R_{load} = 1.6 \Omega$	40	100	680	μs
(di/dt) <sub>on</sub>	Turn-on Current Slope	$R_{load} = 1.6 \Omega$ $V_{CC} = 13 V$	0.008		0.1	A/μs
(di/dt) <sub>off</sub>	Turn-off Current Slope	$R_{load} = 1.6 \Omega$ $V_{CC} = 13 V$	0.008		0.1	A/µs
V <sub>demag</sub>	Inductive Load Clamp Voltage	$R_{load} = 1.6 \Omega$ L = 1 mH	-24	-18	-14	V

### LOGIC INPUT

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
VIL	Input Low Level Voltage				1.5	V
VIH	Input High Level Voltage		3.5		(•)	V
V <sub>I(hyst.)</sub>	Input Hysteresis Voltage		0.2	1	1.5	V
lin	Input Current	$V_{1N} = 5 V$ $T_j = 25 °C$			100	μA
VICL	Input Clamp Voltage	I <sub>IN</sub> = 10 mA I <sub>IN</sub> = -10 mA	5	6 -0.7	7	V V

#### ELECTRICAL CHARACTERISTICS (continued)

PROTECTION AND DIAGNOSTICS (continued)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>STAT</sub>	Status Voltage Output Low	I <sub>STAT</sub> = 1.6 mA			0.4	V
V <sub>USD</sub>	Under Voltage Shut Down		3.5	5	6	V
V <sub>SCL</sub>	Status Clamp Voltage	I <sub>STAT</sub> = 10 mA I <sub>STAT</sub> = -10 mA	5	6 -0.7	7	V V
T <sub>TSD</sub>	Thermal Shut-down Temperature		140	160	180	°C
T <sub>SD(hyst.)</sub>	Thermal Shut-down Hysteresis			15	50	°C
T <sub>R</sub>	Reset Temperature		125			°C
V <sub>OL</sub>	Open Voltage Level	Off-State (note 2)	2.5	3.8	5	V
I <sub>OL</sub>	Open Load Current Level	On-State	0.15		0.85	A
t <sub>povl</sub>	Status Delay	(note 3)		5	10	μs
tpol	Status Delay	(note 3)	50	400	2500	μs

(\*) In= Nominal current according to ISO definition for high side automotive switch (see note 1) (^) See Switchig Time Waveforms

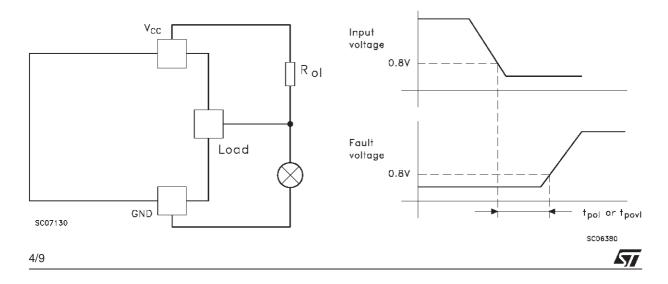
(•) The V<sub>IH</sub> is internally clamped at 6V about. It is possible to connect this pin to an higher voltage via an external resistor calculated to not exceed 10 mA at the input pin.

note 1: The Nominal Current is the current at T<sub>c</sub> = 85 °C for battery voltage of 13V which produces a voltage drop of 0.5 V

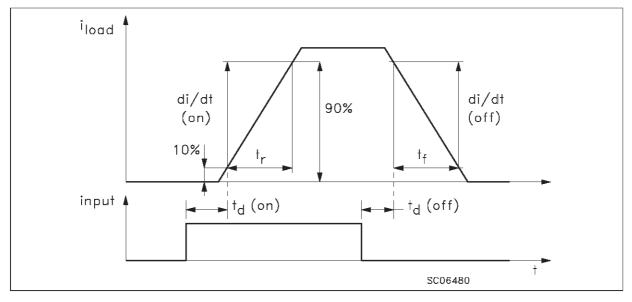
note 2:  $I_{\text{DL}(off)} = (V_{\text{CC}} - V_{\text{DL}})/R_{\text{DL}}$  (see figure) note 3:  $t_{\text{povI}} t_{\text{pol}}$ . ISO definition (see figure)

#### Note 2 Relevant Figure

#### Note 3 Relevant Figure



#### Switching Time Waveforms



#### FUNCTIONAL DESCRIPTION

The device has a diagnostic output which indicates open load in on-state, open load in off-state, over temperature conditions and stuck-on to  $V_{CC}$ .

From the falling edge of the input signal, the status output, initially low to signal a fault condition (overtemperature or open load on-state), will go back to a high state with a different delay in case of overtemperature (tpovl) and in case of open open load (tpol) respectively. This feature allows to discriminate the nature of the detected fault. To protect the device against short circuit and over current condition, the thermal protection turns the integrated Power MOS off at a minimum junction temperature of 140 °C. When this temperature returns to 125 °C the switch is automatically turned on again. In short circuit the protection reacts with virtually no delay, the sensor being located inside the Power MOS area. An internal function of the devices ensures the fast demagnetization of inductive loads with a typical voltage (V<sub>demag</sub>) of -18V. This function allows to greatly reduces the power dissipation according to the formula:

 $P_{dem} = 0.5 \bullet L_{load} \bullet (I_{load})^2 \bullet [(V_{CC} + V_{demag})/V_{demag}]$ • f

where f = switching frequency and

 $V_{demag}$  = demagnetization voltage.

The maximum inductance which causes the chip

temperature to reach the shut-down temperature in a specified thermal environment is a function of the load current for a fixed  $V_{CC}$ ,  $V_{demag}$  and f according to the above formula. In this device if the GND pin is disconnected, with  $V_{CC}$  not exceeding 16V, it will switch off.

# PROTECTING THE DEVICE AGAINST REVERSE BATTERY

The simplest way to protect the device against a continuous reverse battery voltage (-26V) is to insert a Schottky diode between pin 1 (GND) and ground, as shown in the typical application circuit (fig.3).

The consequences of the voltage drop across this diode are as follows:

If the input is pulled to power GND, a negative voltage of  $-V_f$  is seen by the device. (Vil, Vih thresholds and Vstat are increased by Vf with respect to power GND).

The undervoltage shutdown level is increased by Vf.

If there is no need for the control unit to handle external analog signals referred to the power GND, the best approach is to connect the reference potential of the control unit to node [1] (see application circuit in fig. 3), which becomes the common signal GND for the whole control board avoiding shift of  $V_{ih}$ ,  $V_{il}$  and  $V_{stat}$ . This solution allows the use of a standard diode.

## VN16BSP

#### **TRUTH TABLE**

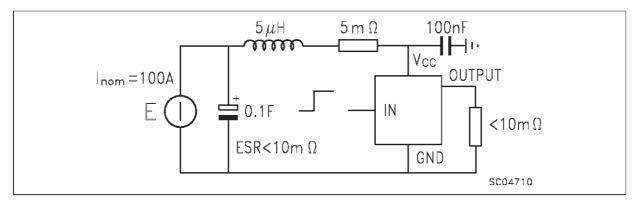
	INPUT	OUTPUT	DIAGNOSTIC
Normal Operation	L	L	Н
Over-temperature	X	L	L
Under-voltage	Х	L	н
Short load to V <sub>CC</sub>	HL	H H	L
Open Load	H	H L	L L (#)

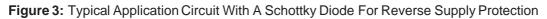
(#) With an additional external resistor

## Figure 1: Waveforms

	NORMAL OPERATION	INPUTOPEN LOAD
	UNDER VOLTAGE	INPUT LIFERMAL STATUS THERMAL SWITCH On Off LIFE THERMAL SWITCH Off LIFE THERMAL SWITCH OF I I 40 °C
INPUT		RTED SC06590

#### Figure 2: Over Current Test Circuit





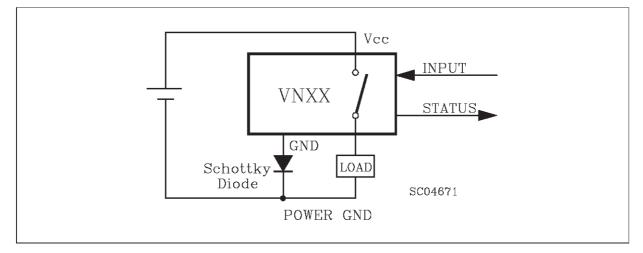
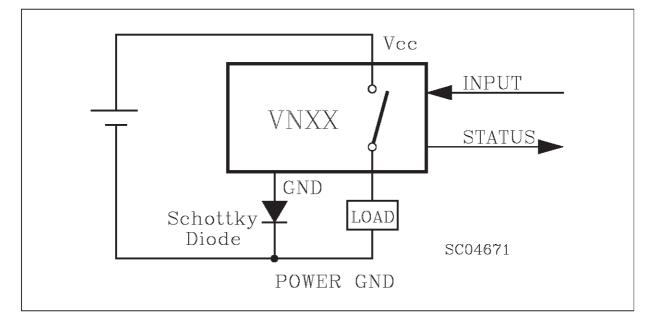


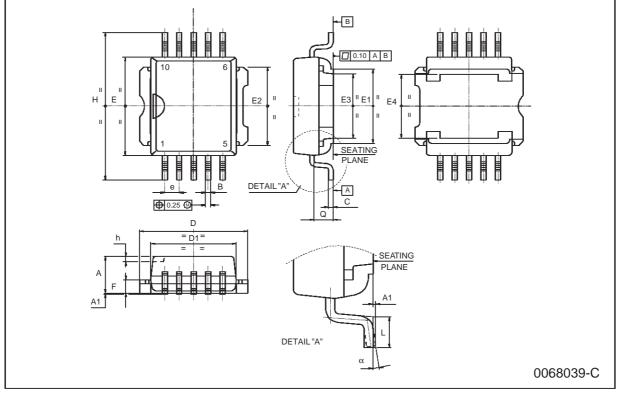
Figure 4: Typical Application Circuit With Separate Signal Ground



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DIM.		mm			inch			
DINI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
А	3.35		3.65	0.132		0.144		
A1	0.00		0.10	0.000		0.004		
В	0.40		0.60	0.016		0.024		
С	0.35		0.55	0.013		0.022		
D	9.40		9.60	0.370		0.378		
D1	7.40		7.60	0.291		0.300		
E	9.30		9.50	0.366		0.374		
E1	7.20		7.40	0.283		0.291		
E2	7.20		7.60	0.283		0.300		
E3	6.10		6.35	0.240		0.250		
E4	5.90		6.10	0.232		0.240		
е		1.27			0.050			
F	1.25		1.35	0.049		0.053		
Н	13.80		14.40	0.543		0.567		
h		0.50			0.002			
L	1.20		1.80	0.047		0.071		
q		1.70			0.067			





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